

an on-screen programming circuit that receives the selected internal or external horizontal and vertical synchronize signals and produces overlaid video signals.

10. The circuit of Claim 9 further including:

a first and second set of buffer circuits, the first set of buffer circuits having inputs coupled to receive the video signals produced by the remotely located computer and outputs coupled to a video monitor of a workstation, the second set of buffer circuits having inputs coupled to receive the video signals produced by the on-screen programming circuit and outputs coupled to the video monitor of the workstation; and

a control logic circuit that enables the first and/or the second sets of buffer circuits so that the video signals supplied to the video monitor of the workstation are either the video signals produced by the remote computer, the video signals produced by the on-screen programming circuit, or both the video signals produced by the remote computer and the on-screen programming circuit.--

Respectfully submitted,

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